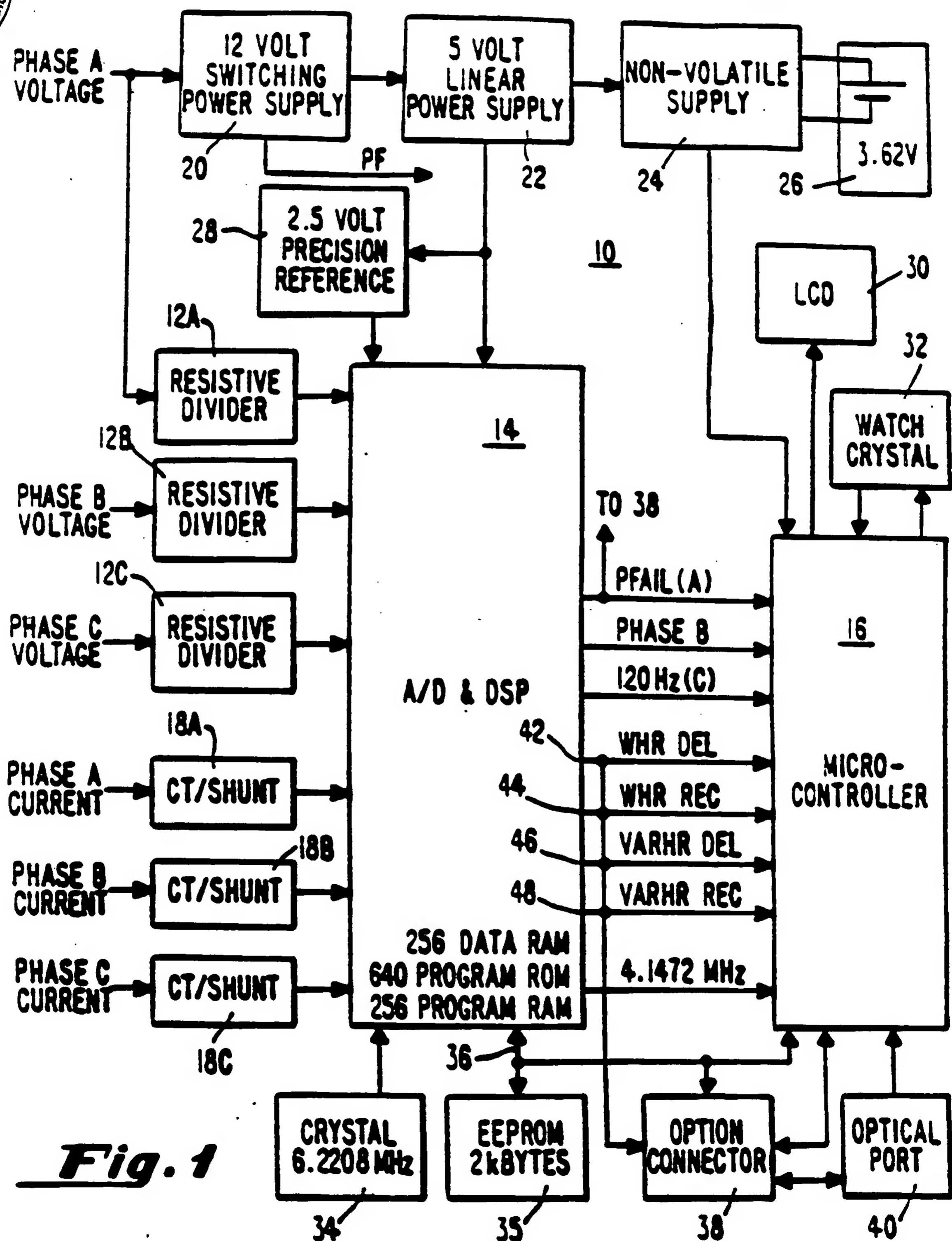




1/6



2/6

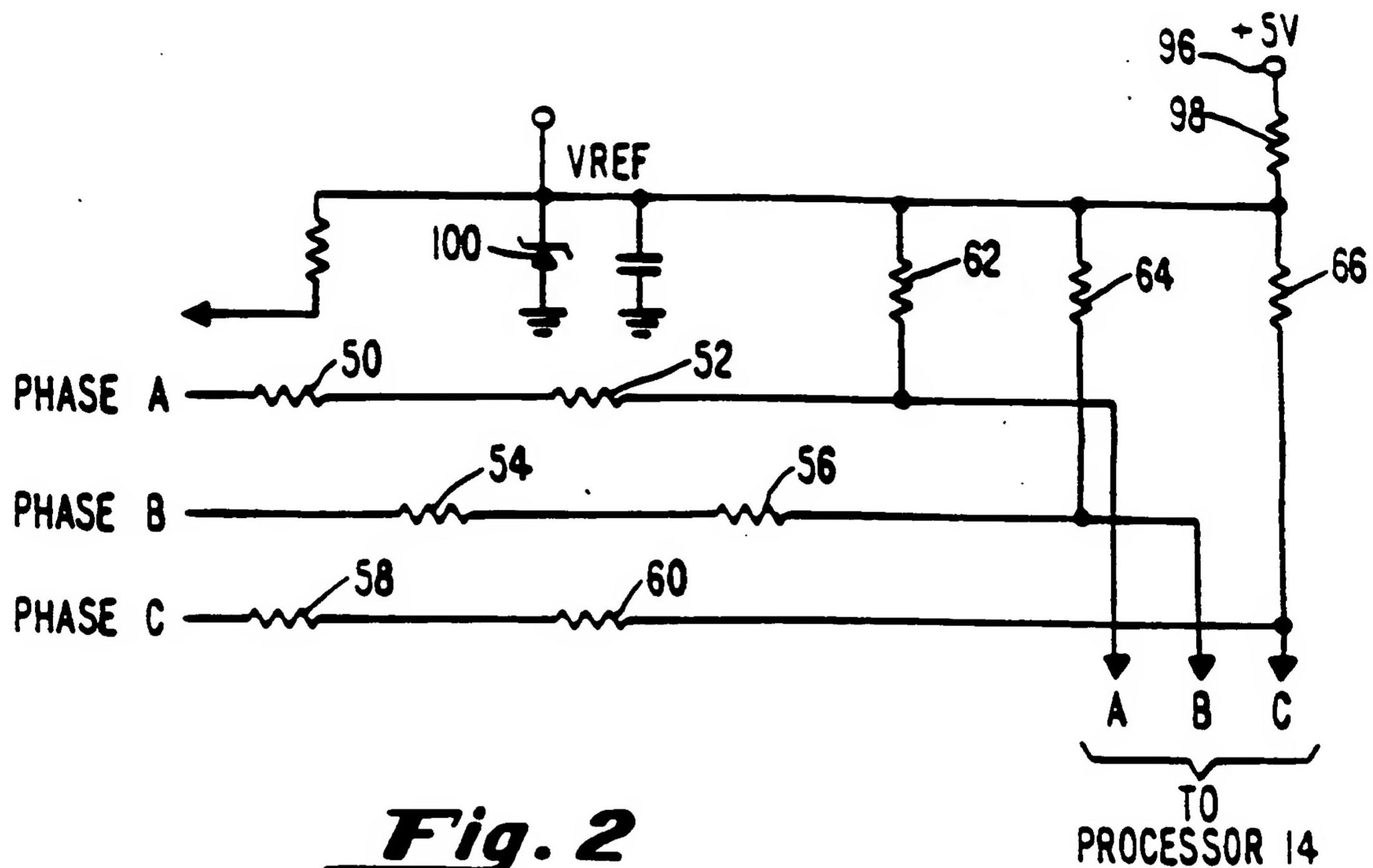


Fig. 2

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PROCESSOR 14

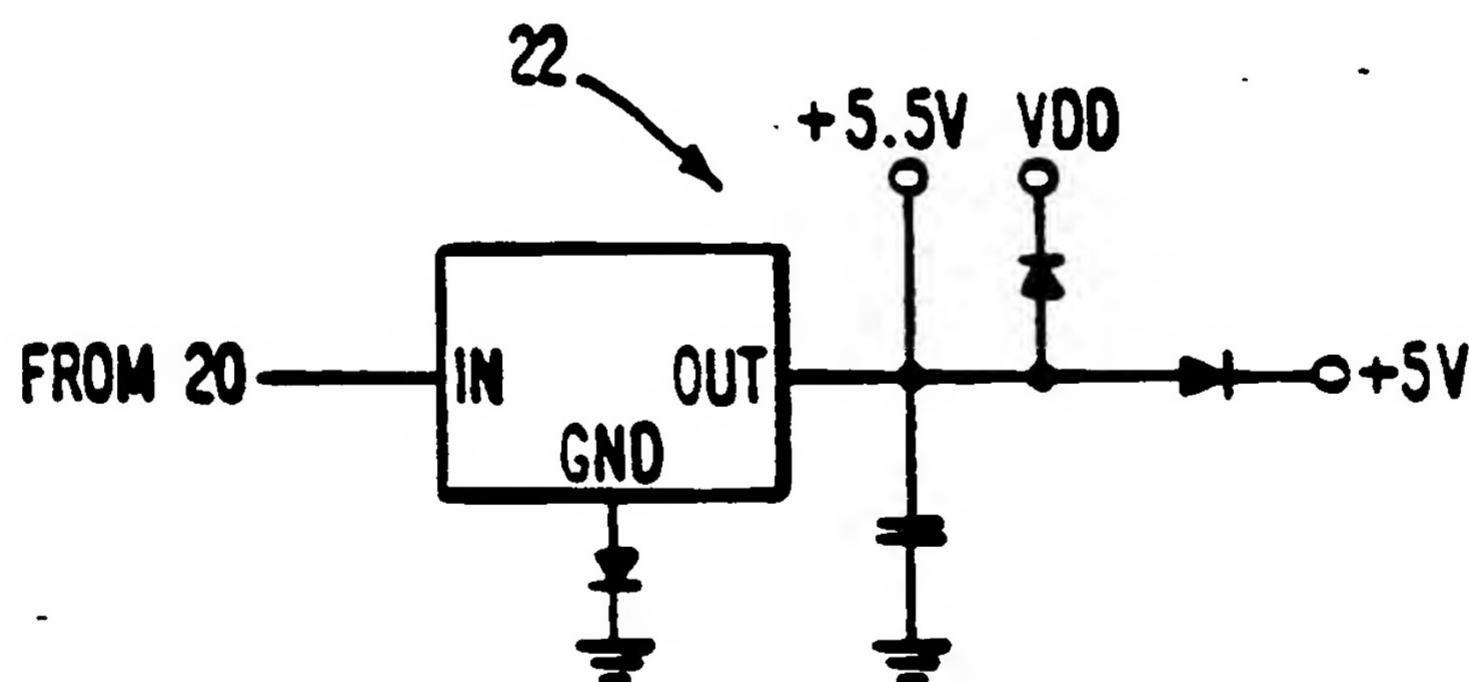
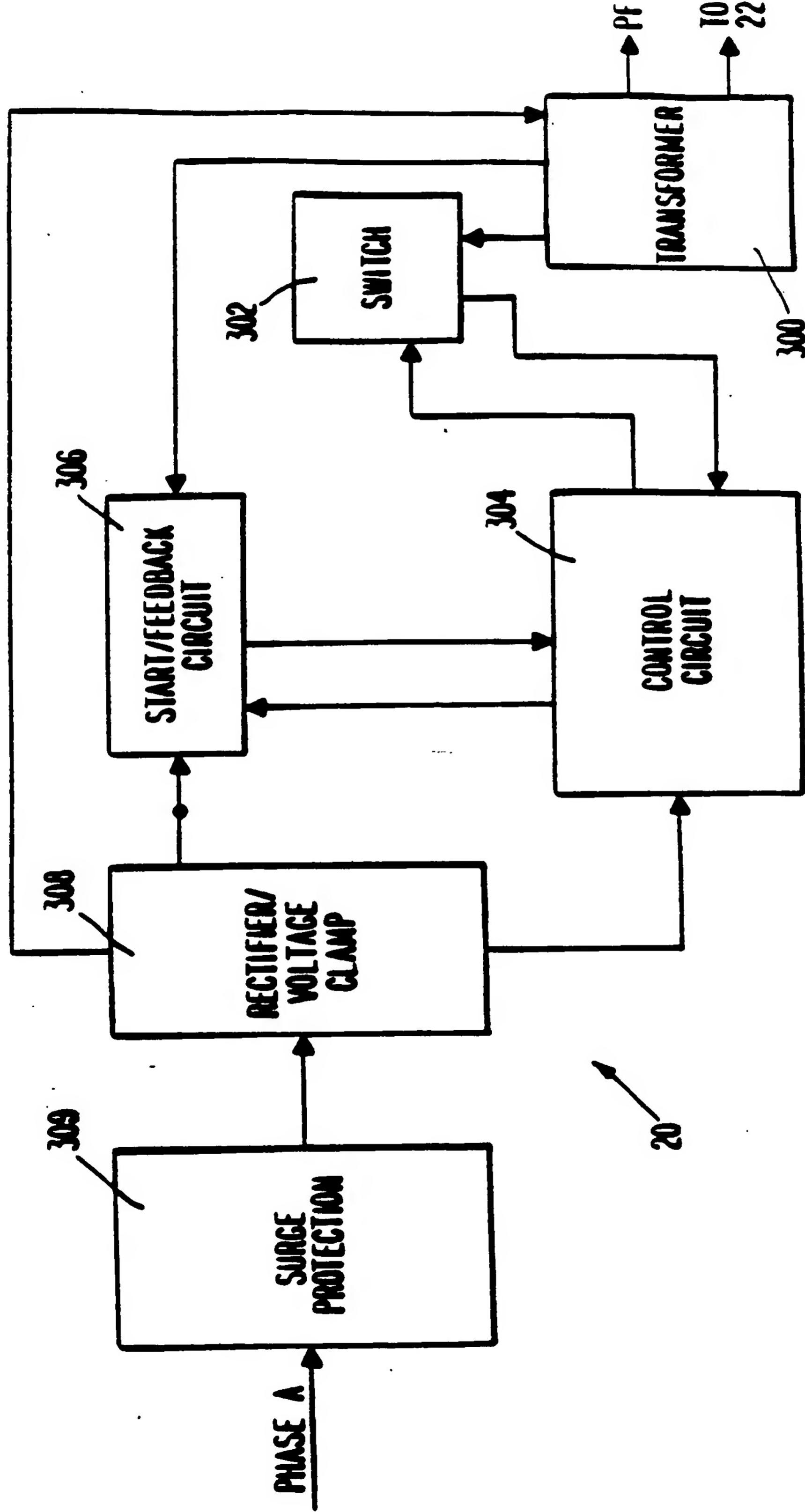
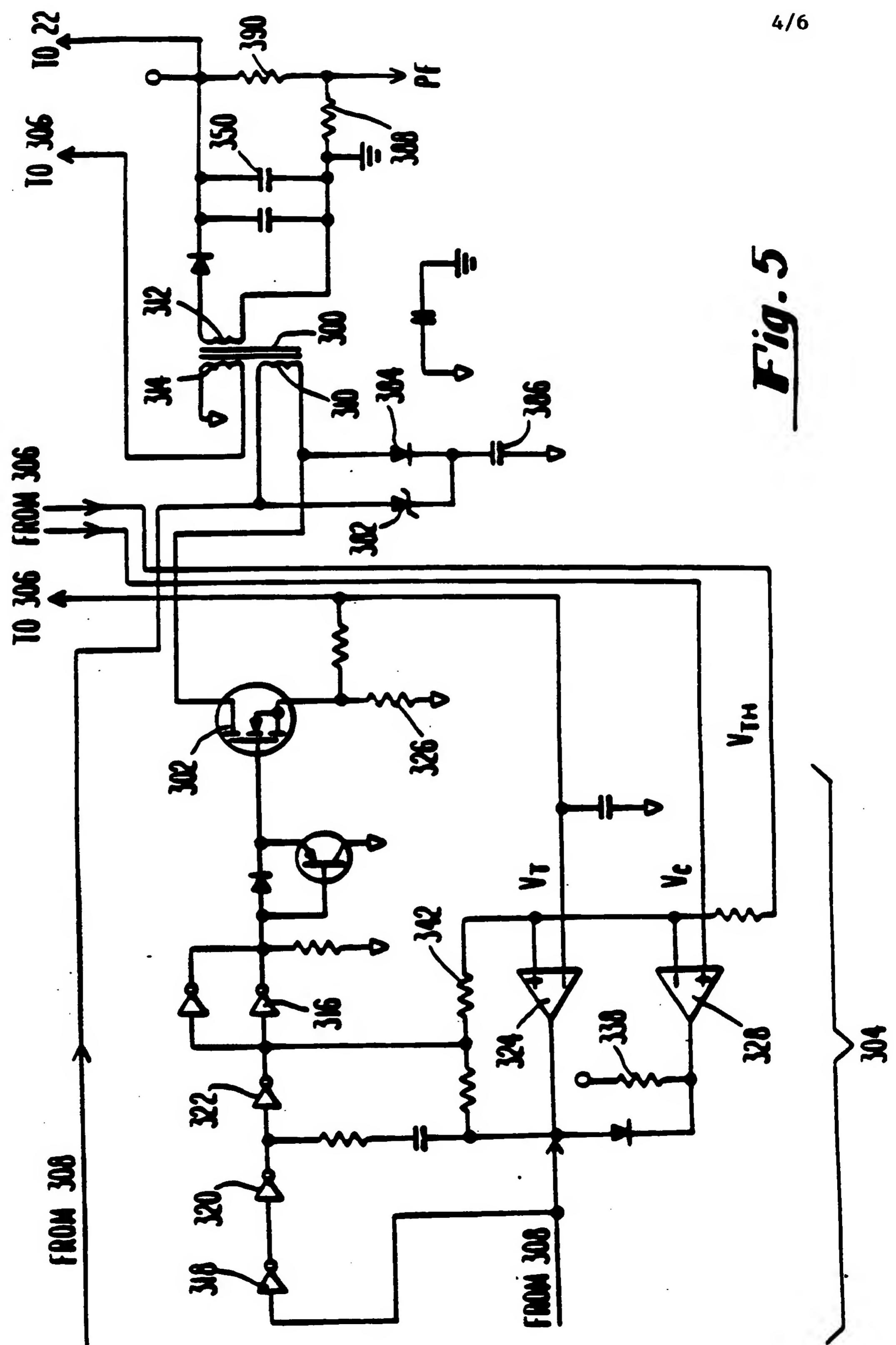


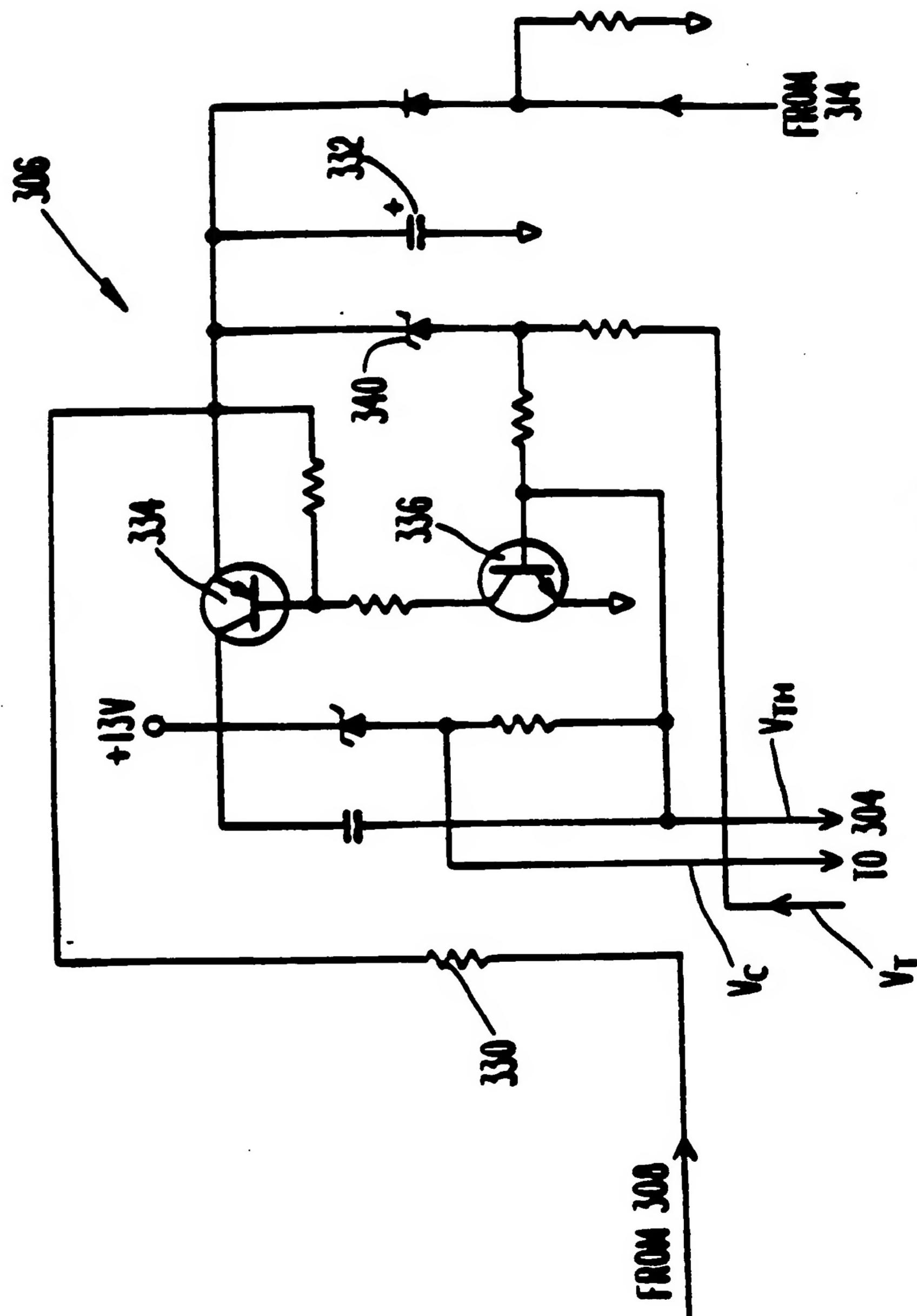
Fig. 3



**Fig. 4**

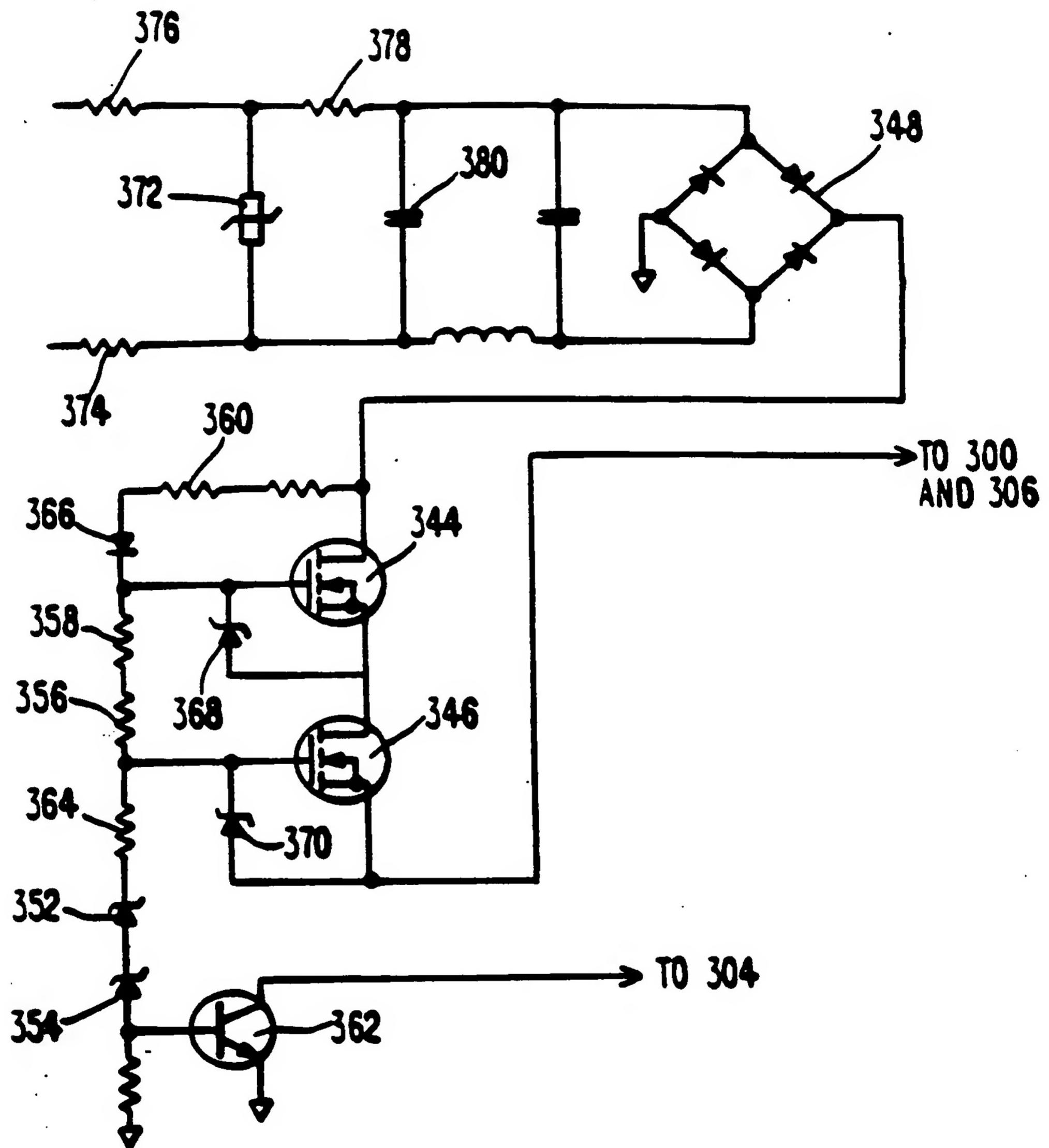


5/6



**Fig. 6**

6/6



**Fig. 7**